

Department of Electronics & Communication Engineering (ECE)

Silicon Institute of Technology, Bhubaneswar

Advertisement for the post of Junior Research Fellow

Applications are invited for the post of Junior Research Fellow (JRF) position on the project "**Methods and mobile applications for interpretation of emotion in various Odia dialects**" funded by the Department of Science and Technology - ICPS, Govt. of India.

Essential Qualification: B.Tech. with valid NET/GATE score, M.Tech. in Electronics Engg/Computer Science & Engg or scholars qualified in National level examinations conducted by Central Govt. Depts. and their agencies and institutions such as DST, IIT, DBT, DRDO, MHRD, ICAR, IISC, IISER, ICMR.

Desirable: Programming skills in signal processing/machine learning algorithms using MATLAB/Python; having skill set in developing Mobile Applications.

Salary, Consolidated: Rs. 25000/- + 20% HRA/month, for two years (Salary will be revised to Rs. 31000/- + 16% HRA/month, if the proposal submitted to DST for grant of additional funds for revised salary is sanctioned).

Age Limit: 30 years

Last Date of Application: 25th Aug 2020

For details visit: contact mswain@silicon.ac.in