TEQIP- III Sponsored Four Days STTP on
“Understanding Power Systems through Hands-on experience”
16th - 19th March, 2020

Organized by
Department of Electrical & Electronics Engineering
Silicon Institute of Technology, Patia, Bhubaneswar – 751024
In association with VTU & AKU

BIJU PATNAIK UNIVERSITY OF TECHNOLOGY

The Biju Patnaik university of Technology took its birth in the year 2003 with the laying of foundation stone on the auspicious hand of late Dr. A. P. J. Abdul Kalam, the ho’ble President of India. The main objective of instituting the University was to ensure a high quality of students coming out of the technical colleges through a common curriculum and uniform evaluation. Riding on the chariot of Public-Private Partnership, the University has grown today to a big family of 137 colleges and covers all gamuts of technical education systems of the country. Today it is proud University who offers Ph.D, Master, and Undergraduate Degrees in the areas of engineering and architecture, business management, computer studies and pharmacy. Today it dreams to introduce B-Voc, M-Voc, and Ph.D-Voc to become an effective partner in the national mission skill development.

ABOUT THE INSTITUTION

Silicon Institute of Technology was established under a duly registered Trust with an objective to provide high quality technical education having significant humanistic aspects. Silicon is determined to build a wholesome institution that would move beyond teaching and learning, with creative leadership and with a culture of teaming. Silicon is constantly innovating and improving to adapt to the needs of students through accepting innovation and embracing modern technology and techniques, thus making it student centric. Silicon’s endeavor has been to make “success” a habit. Silicon’s growth has been possible because of its commitment to excellence and thirst for innovativeness and dynamism. The untiring efforts of a highly dedicated team have been a cornerstone of its success and fast growth.
ABOUT THE DEPARTMENT

Electrical & Electronics Engineering (EEE), deals with the engineering problems, opportunities and needs of the electrical, electronics, computer, telecommunication systems and related industries. This branch provides students with a wide range of fundamental knowledge in core disciplines such as communications, control systems, signal processing, radio frequency design, micro-processors, micro-electronics, power generation and electrical machines. The discipline focuses on design and manufacture of electrical and electronic devices, computers and their component parts, as well as on the integration of components into complex systems. EEE Engineers focus on the analysis, design, development and manufacture of electrical equipment, electronic devices, mechatronics technologies, and automation and control systems.

Modern Electrical Engineers deal with power generation and transmission systems. They can design circuits for electrically operated vehicles, computers, digital devices, electronic memory storage devices, industrial robots and CNC machines.

DEPARTMENT VISION AND MISSION

VISION

To be a premier and value based department committed to excellence so as to model electrical engineers to meet the challenges of the emerging trends in technical world.

MISSION

1. To impart fundamental engineering knowledge and academic discipline for sustained and successful professional career that will help in enhancing the nation’s education. (Knowledge and Continuing education).
2. To promote creativeness, innovativeness and out-of-box thinking among students, improving research ability to maintain technical proficiency (Innovations and Research).
3. To inculcate professional ethics and human values, to take mantle of higher responsibilities in technical and social domain.

OBJECTIVES OF THE STTP

In the last two decades, interconnected network complexity, integration of renewable energy sources, compensating devices, power electronics interfaces and communications have been progressively applied to improve the power availability, operational efficiency and utilization. However, such substantial development in the power grid introduces new challenges for the operator in the load dispatch centers to have proper monitoring, control and protection of the power system.

The Short-term Training Program (STTP) presents the views of prominent experts from academia and industry and provides hands-on practice for different power system operation like optimal power flow, short circuit analysis, protection, etc., through modern power system analysis software tools.

RESOURCE PERSONS

The Resource Persons will be faculty members from Leading Academic Institutes and Industrial Experts.

ELIGIBILITY

The STTP is open to faculty members/research scholars and students from AICTE Approved Institutions in the Department of EE/EEE/ECE/ETC/CSE/AE&IE/E&IE.

SELECTION CRITERIA

The maximum number of participants will be 60. Kindly email the filled registration form to coordinators at the earliest. Selection will be done on a first-cum-first-serve basis and confirmed candidates will be notified immediately.
REGISTRATION FEE

Registration fee is free for TEQIP Institutes (Rs. 1000 for Non-TEQIP Institutes). Payment shall be made through demand draft (DD) or online mode. The DD should be in favour of Silicon Institute of Technology payable at Bhubaneswar. For online payment, the account details are given as:

A/C Holder: Silicon Institute of Technology
A/C No.: 380802011000162
Types of Account: Savings
Bank Name: Union Bank of India
Branch Name: Infocity, Bhubaneswar
IFSC Code: UBIN0559512

REGISTRATION AND ACCOMODATION

Bus facility is available from all major parts of the city. Accommodation facility will be provided to the selected outstation participants as required.

CHIEF PATRON
Dr. C. R. Tripathy
Vice-Chancellor, BPUT, Rourkela, Odisha

PATRONS
Dr. S. K. Misra
Director, SIT, Bhubaneswar
Dr. J. Talukdar
Principal, SIT, Bhubaneswar
Dr. J. Das
Dean (Research), TEQIP-III Coordinator, SIT, Bhubaneswar
Dr. D. Kar
Dean (Academics), SIT, Bhubaneswar

ORGANIZING CHAIR
Dr. Ramaprasad Panda
Professor, Dept. of EEE, SIT, Bhubaneswar

WEB CHAIR
Prof. Umasankar Das
Sr. Asst. Prof., Dept. of MCA, SIT, Bhubaneswar

PROGRAMME CO-ORDINATORS
Dr. Ranjan Jena, BPUT, Odisha
Prof. Laxmi Narayan Pathy, HOD, EEE
Prof. Gyana Ranjan Biswal, Asst. Prof., EEE
Dr. Tapas Kumar Maji, Asst. Prof., EEE
ORGANIZING COMMITTEE
Dr. Shivakumar Mishra, CAPGS, BPUT
Dr. Bandan Panigrahi, CAPGS, BPUT
Dr. Rabi Narayan Mishra, Sr. Asst. Prof., EEE
Dr. Rajan Kumar Mishra, Asst. Prof., EEE
Dr. Priyanka Kar, Assoc. Prof., EEE

ADVISORY COMMITTEE
Prof. Manas R. Nayak, Nodal officer (Academics), BPUT
Prof. Smrutirekha Sahoo, Nodal officer (Finance), BPUT
Prof. Himanshu Sekhar Behera, Registrar, BPUT
Dr. R. Nagaraja, C. M. D., PRDC Infotech. Ltd.
Dr. Pradyumna Tripathy, Assoc. Professor, HOD, CSE

To download the Brochure, click here

Registration (Last Date: 13th March, 2020)

For Online registration, click on the link below:
https://docs.google.com/forms/d/e/1FAIpQLSfbhZXVrsnO01VI5_YyDif1xQ0YsxJFvcnwE6L6UQ98ve4Jw/viewform

For Offline registration, click here
(Download the registration form and send the duly filled scanned copy to the email ID: tapas.maji@silicon.ac.in)

For queries, contact
1. Prof. Gyana Ranjan Biswal
   Mobile: 9040140469
   Email: gyanaranjan.biswal@silicon.ac.in

2. Dr. Tapas Kumar Maji
   Mobile: 9126507481 / 7001577529
   Email: tapas.maji@silicon.ac.in